

## Interface between modem and subscriber interface module (SIM)

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Inventor: IV J A HUTCHISON (US); SUTANKAYO S I (US)

Applicant: QUALCOMM INC (US)

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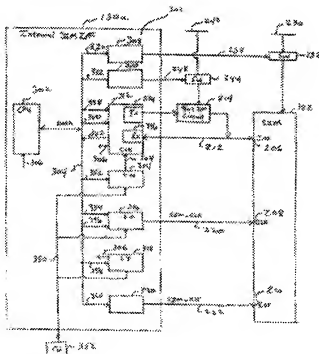
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A circuit for interfacing a modem in a wireless communication device to a Subscriber Interface Module (SIM) (122) includes a modem controller (302), a Universal Asynchronous Receiver/Transmitter (UART) (312) connected to the modem controller, a programmable clock circuit (314, 316) adapted to generate a SIM clock and a UART clock independently of one another based on a common clock (350) and a control signal (352) provided to the programmable clock circuit, a reset circuit (320) to generate a SIM reset signal, and a power control circuit (308) to control powering on and off the SIM, all on an integrated circuit chip (301). The circuit also includes a Bus Interface circuit (214) to interface the modem to the SIM using a common data line (212).



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